

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

09/903,059

Confirmation No.:

9375

First Named Inventor: Bulucea, Constantin

Filing Date:

10 July 2001

Group Art Unit:

2891

Examiner:

Farahani, D.

Atty. Docket No.:

NS-4971 US

Title:

Gate-Enhanced Junction Varactor

Assignee(s):

National Semiconductor Corporation

Mountain View, California 6 June 2006

MAIL STOP AMENDMENT **COMMISSIONER FOR PATENTS** PO Box 1450 Alexandria, Virginia 22313-1450

AMENDMENT TO DRAWINGS

Sir:

The drawings of the above patent application should be amended in the following manner.

In each of Figs. 19b and 23b, reference symbol "V_{DD}" should be changed to "V_{HH}".

In Fig. 22, change "C1 Cap. Signal Path" and "C2 Cap. Signal Path" respectively to "C1 Capacitance Signal Path" and "C2 Capacitance Signal Path".

Enclosed are copies of the relevant drawings sheets in which the foregoing changes are indicated in red.

Drawings changes previously requested and incorporated into the formal drawings submitted 27 January 2003 are indicated in pencil in the enclosed copies of the relevant drawing sheets.

Also enclosed, in conjunction with the explanation presented in the Remarks, is a copy of the drawing sheet containing Fig. 22 in which previously made changes to Fig. 22 are indicated in red.

Ronald J. Meetin Attorney at Law 210 Central Avenue Mountain View, CA 94043-4869

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REMARKS

Applicant's attorney has requested that the drawings be amended to correct a pair of self-evident errors. In particular, Applicant's attorney has requested that reference symbol " V_{DD} " for the high voltage supply in each of Figs. 19b and 23b be changed to " V_{HH} " in conformity with the material presented in paragraphs 186, 192, 240, and 245 of the specification.

In the Amendment submitted 8 January 2003 for revising the drawings, Applicant's Attorney requested that reference symbol " V_{SS} " for the low voltage supply in each of Figs. 19b and 23b be changed to " V_{LL} " but did not notice that reference symbol " V_{DD} " for the high voltage supply in each of Figs. 19b and 23b should similarly be changed to " V_{HH} ". The present amendment corrects that error.

Applicant's Attorney notes that the abbreviation "Cap." in the labels "C1 Cap. Signal Path" and "C2 Cap. Signal Path" in Fig. 22 was changed to "Capacitance" in the formal version of Fig. 22 submitted 27 January 2003, thereby bringing Fig. 22 into conformity with the use of the labels "C1 Capacitance Signal Path" and "C2 Capacitance Signal Path" in Fig. 18. To the extent that a request to spell out the abbreviation "Cap." in Fig. 22 should have been previously submitted to the PTO, Applicant's Attorney hereby requests approval for changing "C1 Cap. Signal Path" and "C2 Cap. Signal Path" in Fig. 22 respectively to "C1 Capacitance Signal Path" and "C2 Capacitance Signal Path".

Please telephone Attorney for Applicant at 650-964-9767 if there are any questions.

EXPRESS MAIL LABEL NO.:

EV 743 585 153 US

Respectfully submitted,

Ronald J. Meetin

Attorney for Applicant(s)

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SUBMISSION OF CORRECTED FORMAL DRAWINGS

Sir:

Enclosed are replacements for formal drawing sheets 12 and 17 of the above patent application. Enclosed replacement formal drawing sheets 12 and 17 incorporate the revisions requested in Figs. 19b and 23b via the accompanying Amendment to Drawings. If these revisions are acceptable, please substitute enclosed replacement drawing sheets 12 and 17 for formal drawing sheets 12 and 17 submitted 27 January 2003.

Please telephone Attorney for Applicant(s) at 650-964-9767 if there are any questions regarding these drawings.

EXPRESS MAIL LABEL NO.:

EV 743 585 153 US

Respectfully submitted,

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Appl'n. No.: 11/407,025

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6 June 2006

MAN STOP AMENDMENT

PO Box 1450

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Application No.:

09/903,059

Confirmation No.:

Filing Date:

Examiner:

9375

First Named Inventor:

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10 July 2001 Farahani, D.

Group Art Unit: Atty. Docket No.: 2891

NS-4971 US

Title: Assignee(s):

Gate-Enhanced Junction Varactor National Semiconductor Corporation

Sir:

Transmitted herewith are the following documents for the above patent application:

- 1. Return Receipt Postcard;
- 2. This Transmittal Letter (in duplicate);
- 3. Supplemental Amendment (4 pp.);
- 4. Marked-up Specification (79 pp.);
- 5. Substitute Specification (79 pp.);

- 6. Amendment to Drawings (2 pp.);
- 7. Annotated Drawing sheets (3 pp.).
- 8. Submission of Corrected Formal Drawings (1 pp.); and
- 9. Replacement Drawing sheets (2 pp.).

The fee has been calculated as shown below:

CLAIMS AS AMENDED

				Highest No.						
		Claims Remaining		Previously		Present				
		After Amendment		Paid For		Extra		Rate		Additional Fee
Total	Claims	97	Minus	114	=	0	х	\$50.00	\$	0.00
Indep	endent									
Clain	าร	7	Minus	9	=	0	х	\$200.00	\$	0.00
	Fee of \$360 for the first filing of one or more multiple dependent claims								\$	
	Fee for Request for Extension of Time (month(s))								\$	
	Fee for								\$	
	Total a	dditional fee for thi	s Amendm	ent:					\$	0.00
\boxtimes	Please charge Deposit Account No. 502641 in the amount of								\$	0.00
	Conditional Petition for Extension of Time: If an extension of time is required, the Commissioner is authorized to deduct the necessary fee from Deposit Account No. 502641.								_	
\boxtimes	Also, charge any additional fees required and credit any overpayment to Deposit Account No. 502641.									

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